

TABLE OF CONTENTS

Table of Contents	iii
Preface	vii
About This Manual	vii
Documentation Conventions	viii
Overview	1
1.1 The PSCAD Modules	2
Before You Begin	5
2.1 Terms	5
2.1.1 Interface Objects	5
2.1.2 The Pointer and Mouse	5
2.1.3 Highlighting	5
2.1.4 Dragging and Clicking	6
2.1.5 Disabled Objects	6
2.1.6 Icons	6
2.2 Basic Interface Objects	7
2.2.1 Buttons	7
2.2.2 Menus and Menu Buttons	7
2.2.3 Input Box	8
2.2.4 Selection Interface Objects	8
2.3.1 Requesting Help	9
2.3.2 Getting Around	10
2.3.3 Exiting Help	11
2.3.4 Bookmarks	12
The Voltage Divider	13
3.1 Starting the PSCAD FileManager	13
3.1.1 Creating a Project	14
3.1.2 Opening a Project	15
3.1.3 Creating & Opening a Case	15
3.2 Creating the Circuit Diagram	16
3.2.1 General Sketching Techniques	17
3.2.2 Sketching the Voltage Divider Circuit	22
3.2.3 Entering Circuit Parameters	26
3.2.4 Entering the Subsystem Properties	27
3.2.5 Printing the Circuit Drawing	27
3.2.6 Saving the Circuit	28
3.2.7 Compiling the Circuit	28
3.2.8 Iconifying Draft	29

3.3	Performing the Simulation using RunTime	30
3.3.1	Setting up the EMTDC Operator's Console	31
3.3.2	Creating Monitoring Interface Objects	32
3.3.3	Creating Control Interface Objects	36
3.3.4	Starting the Simulation	39
3.3.5	Interacting with the Simulation	40
3.3.6	Ending the Simulation	41
3.4	Plotting and Analyzing the Results using MultiPlot	42
3.4.1	Adding Graphs to a Page	43
3.4.2	Adding Text to a Page	48
3.4.3	Printing the MultiPlot File	49
3.4.4	Saving the MultiPlot File	49
3.4.5	Updating Graphs	49
3.5	Terminating the PSCAD Session	50
A Simple AC Power System	51
4.1	Overview	51
4.2	Creating a Case Directory	51
4.3	Preparing the Transmission Line Data	52
4.4	Preparing the Power System Circuit using Draft	54
4.5	Performing the Simulation using RunTime	60
A Simple HVDC System	61
5.1	The Six Pulse HVDC Valve Group Model	61
5.2	Building the System Model	62
5.2.1	The Rectifier Circuit Portion	62
5.2.2	The Inverter Circuit Portion	65
5.2.3	A Very Simple Line Model	66
5.2.4	Rectifier and Inverter Controls	67
5.2.5	Compile and Save	68
5.3	Performing the Simulation	69
Network Frequency Scan	71
6.1	Frequency Scan of a Series RLC Branch	72
6.1.1	Creating the Circuit	72
6.1.2	Performing the Simulation	74
6.1.3	Analyzing the Results	75
6.2	Frequency Scan of a HVDC System Commutating Bus	78
6.3	Frequency Scan Restrictions	79
The Cigre Benchmark Model	81
7.1	Sending and Receiving End AC Systems	82
7.2	Valve Groups and DC System	83
7.3	Rectifier and Inverter Controls	85

7.4	Run-up to Steady-state Operating Conditions	87
7.5	Running from the Snapshot	88
Advanced Run Features		89
8.1	Multiple Runs	89
8.1.1	Creating Variable Parameters	89
8.1.2	Creating a Goal Signal	90
8.1.3	Performing a Multiple Run	91
8.2	Background Runs	93
8.2.1	Placing a Run in the Background	93
8.2.2	Producing Reports from Background Runs	94
Appendix A: Keyboard Shortcuts		95
A.1	Forms	95
A.2	File Requestors	95
A.3	Help	96
Index		97

Table of Contents