908 Memory Switches

Closed = 0

Must be closed 2^4 2^3 2^2 2^1 2^0

Dataway $\longleftarrow$ | 1   2   3   4   5   6 | $\longrightarrow$ Front pannel

MSB   LSB

one **+** number represented by switches = number of 32K memory modules

---

903 Memory Switches

Closed = 0

2^0 2^1 2^2 2^3 2^4

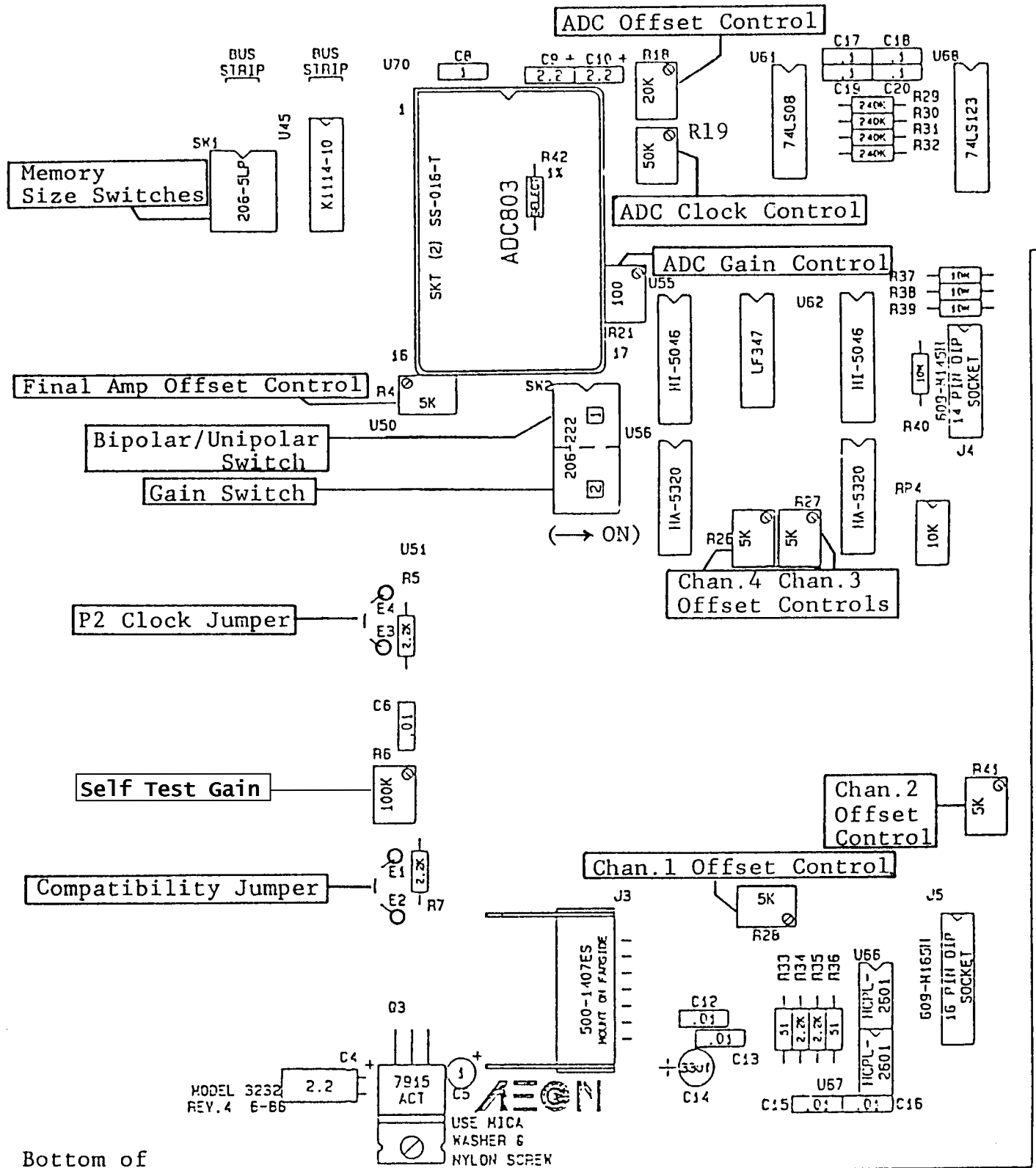Dataway $\longleftarrow$ | 1   2   3   4   5 | $\longrightarrow$ Front pannel

LSB   MSB
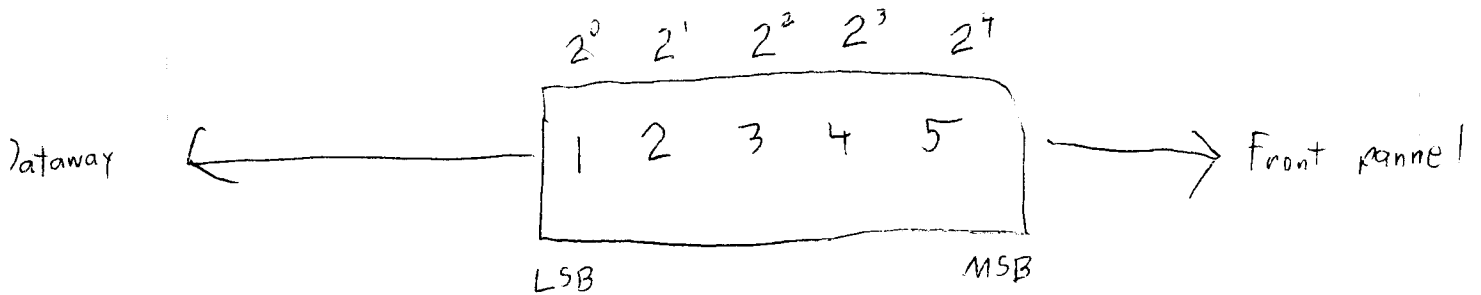
number represented by switches = number of 32K modules or <u>below</u> this one, or
32K × number represented by switches = starting address of module

3232 Calibration Pot Locations

Top of Control (left) board

Bottom of
Control (left) board   (Some components deleted for clarity)

Figure 2-1. Switch, Jumper and Controls Positions

3232 Switch Settings

B. Memory Size

The size of available memory is set by the five dip switches located on the side of the module. The switches are set as follows:

Switch 1	Switch 2	Switch 3	Switch 4	Switch 5	Memory Size
ON	ON	ON	ON	ON	32K
ON	ON	ON	ON	OFF	64K
ON	ON	ON	OFF	ON	96K
ON	ON	ON	OFF	OFF	128K
ON	ON	OFF	ON	ON	160K
ON	ON	OFF	ON	OFF	192K
ON	ON	OFF	OFF	ON	224K
ON	ON	OFF	OFF	OFF	256K
ON	OFF	ON	ON	ON	288K
ON	OFF	ON	ON	OFF	320K
ON	OFF	ON	OFF	ON	352K
ON	OFF	ON	OFF	OFF	384K
ON	OFF	OFF	ON	ON	416K
ON	OFF	OFF	ON	OFF	448K
ON	OFF	OFF	OFF	ON	480K
ON	OFF	OFF	OFF	OFF	512K
OFF	ON	ON	ON	ON	544K
OFF	ON	ON	ON	OFF	576K
OFF	ON	ON	OFF	ON	608K
OFF	ON	ON	OFF	OFF	640K
OFF	ON	OFF	ON	ON	672K
OFF	ON	OFF	ON	OFF	704K
OFF	ON	OFF	OFF	ON	736K
OFF	ON	OFF	OFF	OFF	768K
OFF	OFF	ON	ON	ON	800K
OFF	OFF	ON	ON	OFF	832K
OFF	OFF	ON	OFF	ON	864K
OFF	OFF	ON	OFF	OFF	896K
OFF	OFF	OFF	ON	ON	928K
OFF	OFF	OFF	ON	OFF	960K
OFF	OFF	OFF	OFF	ON	992K
OFF	OFF	OFF	OFF	OFF	1024K

Input range	Switch 1	Switch 2
0 to +10.24 Volts	OFF	OFF
-10.24 to +10.24 Volts	ON	OFF
0 to +5.12 Volts	OFF	ON
-5.12 to +5.12 Volts	ON	ON

A18 offset

O

A19 ADC Clock

O

A21 Gain

O

down = external momentary

down = internal momentary

Bipolar strapping

Unipolar strapping

gain On=10V off=5V

on=unipolar off=bipolar

LSB   MSB

2^0 2^1 2^2 2^3 (on=1)
Channel select

R1   ADC clock
R3   gain for all ranges except ±10
P2   ±10v gain
R4   offset

907C

☐ R17   gain for ±10V only
☐ R19   gain for all other ranges
☐ R20   offset
☐ R26   ADC Clock
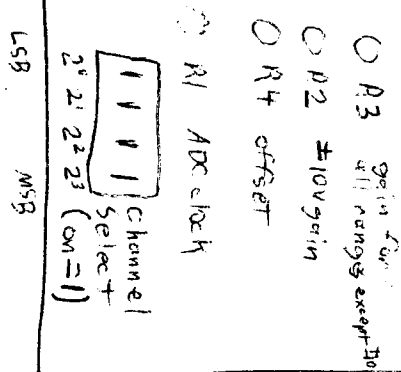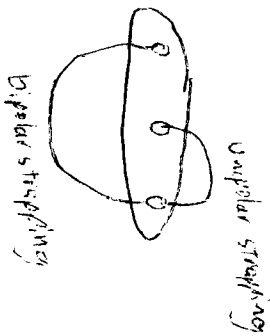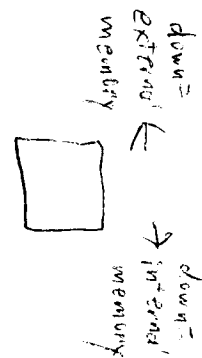
10-20-86

appendix B

" *YELLOW DOT* " *SWITCH SETTINGS*

912/914
  ↳ *2 MHz*
  ↳ *500 KHz*

on off

label	#
nu	1
nu	2
delay control	3
delay disable	4
mem1	5
mem0	6
H912/914	7

BA32

	8k	32k	64k	128k
mem1	on	on	off	off
mem0	on	off	on	off

	912	914
H912/914	on	off

delay control

	sw3	sw4	mode
invalid	on	on	not used
software control	on	off	~~master(1)/slave(0)~~
hardware control	off	on	delay disabled (slave)
harddware control	off	off	delay enabled (master)