

Design Review Documentation - Results No.:

Title: Shorted Turn Protection FDR

CAT: ☒A1 ☐A2 ☐A3

Type of Review: ☐Peer ☐CDR ☐PDR ☒FDR

Cognizant Individual: G. Tchilinguirian

Date of Review: 1/23/20

Review Board Members:

Chair: T. Stevenson

RE J. Dellas

CE R. Ellis

TA S. Gerhardt

TA P.

Dugan

TA P. Sichta

TA R. Camp

TA C. Freeman

TA D. Battaglia

TA W. Que

PE Y. Zhai

QA A. Castaneda

Attendees:

P. Bisbal

D. Boyer

J. Corl

J. Galayda

R. Hawryluk

F. Hoffman

A. Indelicato

J. Landi

R. Rosenblat

N. Rahman

W. Reese

F. Yang

Attendees:

G. Zimmer

Items Reviewed:	Sat.	Unsat.	Comments or n/a if not applicable
Appropriate requirements identified	X	<input type="checkbox"/>	
Development plans and schedules	X	<input type="checkbox"/>	
Reg. compliance incl. USI/USID and NEPA	X	<input type="checkbox"/>	
Disposition of CHITS from previous reviews	X	<input type="checkbox"/>	
Calculations (all listed are signed and filed)	X	<input type="checkbox"/>	
Cost objectives	X	<input type="checkbox"/>	
Other review objectives addressed	<input type="checkbox"/>	<input type="checkbox"/>	n/a

SUMMARY OF RESULTS:

The purpose of this final design review (FDR) was to present sufficient technical material for review to prove that the STP can be implemented in a manner that satisfies project requirements and has an achievable cost and schedule acceptable to the NSTX-U Recovery Project. To reach this level of design maturity, there has been further progress since conceptual, preliminary, and peer reviews to present steps leading towards this final design. Chits attained during previous reviews have been addressed, shaping the final design of the system. A report of all CHITS preceding this design and how they were addressed is included in the project documentation.

Some of the major issues that have been addressed in the process of refining the Preliminary design were related to the redundant measurement of voltages at the FCPC SDS cabinets, the use of the existing FPDP infrastructure to communicate faults, the use of the existing DCPS interconnection and test harnesses to certify the system for use.

The FDR covered the real-time design of components needed to measure, model and evaluate the coil behavior, detect faults, and signal a failure to FCPC's fault system to take appropriate action. A final design for a test harness to functionally test the Shorted Turn Protection system using the Autotester was also presented. Ten minor chits were generated.

Disposition: [check one]

- ☐ **Acceptable**
- ☒ **Acceptable pending resolution of concerns-** CHITS identified above must be resolved prior to installation.
- ☐ **Incomplete** - Additional design work is required prior to another design review.
- ☐ **Unsuccessful** – Corrective actions must be taken and another review process must be initiated.

Design Review Chair:

Date:

Cognizant Individual Acceptance

Date:

Distribution: Review Board Members, Operations Center, Responsible Engineer (RE), Cognizant Individuals, Project Manager, Project Director, relevant Technical Authorities (TAs), Chief Engineer (CE), Fire Protection Engineer, Attendees, QA, ES&H, Security, Requesting & Performing Dept. Head